

# Molding Type Module IGBT, Chopper in 1 Package, 1200 V and 50 A



INT-A-PAK

PRIMARY CHARACTERISTICS					
V <sub>CES</sub>	1200 V				
I <sub>C</sub> at T <sub>C</sub> = 80 °C	50 A				
V <sub>CE(on)</sub> (typical) at I <sub>C</sub> = 50 A, 25 °C	1.7 V				
Speed	8 kHz to 30 kHz				
Package	INT-A-PAK				
Circuit configuration	Low side chopper				

#### **FEATURES**

- High short circuit capability, self limiting to 6 x I<sub>C</sub>
- 10 µs short circuit capability
- Low inductance case
- Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- V<sub>CE(on)</sub> with positive temperature coefficient
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912"><u>www.vishav.com/doc?99912</u></a>

#### **TYPICAL APPLICATIONS**

- AC inverter drives
- Switching mode power supplies
- Electronic welders

#### **DESCRIPTION**

Vishay's IGBT power module provides ultralow conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Collector to emitter voltage	V <sub>CES</sub>		1200	V
Gate to emitter voltage	$V_{GES}$		± 20	V
Collector current		T <sub>C</sub> = 25 °C	100	
Collector current	Ic	T <sub>C</sub> = 80 °C	50	
Pulsed collector current	I <sub>CM</sub> <sup>(1)</sup>	t <sub>p</sub> = 1 ms	100	А
Diode continuous forward current	I <sub>F</sub>		50	
Diode maximum forward current	I <sub>FM</sub>		100	
Maximum power dissipation	P <sub>D</sub>	T <sub>J</sub> = 150 °C	446	W
Short circuit withstand time	t <sub>SC</sub>	T <sub>J</sub> = 125 °C	10	μs
l <sup>2</sup> t-value, diode	l <sup>2</sup> t	$V_R = 0 \text{ V}, \text{ t} = 10 \text{ ms}, T_J = 125 ^{\circ}\text{C}$	420	A <sup>2</sup> s
RMS isolation voltage	V <sub>ISOL</sub>	f = 50 Hz, t = 1 min	2500	V

#### Note

<sup>(1)</sup> Repetitive rating: pulse width limited by maximum junction temperature

IGBT ELECTRICAL SPECIFICATIONS (T <sub>C</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V <sub>(BR)CES</sub>	T <sub>J</sub> = 25 °C	1200	-	-	
Collector to emitter saturation voltage	V	$V_{GE} = 15 \text{ V}, I_{C} = 50 \text{ A}, T_{J} = 25 ^{\circ}\text{C}$	- 1	1.70	-	V
Collector to emitter saturation voltage	V <sub>CE(on)</sub>	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 50 A, T <sub>J</sub> = 125 °C	-	1.95	-	V
Gate to emitter threshold voltage	V <sub>GE(th)</sub>	$V_{CE} = V_{GE}$ , $I_C = 2$ mA, $T_J = 25$ °C	5.0	6.2	7.0	
Zero gate voltage collector current	I <sub>CES</sub>	$V_{CE} = V_{CES}$ , $V_{GE} = 0$ V, $T_{J} = 25$ °C	-	-	1.0	mA
Gate to emitter leakage current	I <sub>GES</sub>	$V_{GE} = V_{GES}$ , $V_{CE} = 0$ V, $T_{J} = 25$ °C	-	-	400	nA



SWITCHING CHARACTERISTICS	<b>S</b>					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t <sub>d(on)</sub>		-	220	-	ns mJ
Rise time	t <sub>r</sub>		-	60	-	
Turn-off delay time	t <sub>d(off)</sub>	$V_{CC} = 600 \text{ V}, I_{C} = 50 \text{ A}, R_{q} = 18 \Omega,$	-	420	-	
Fall time	t <sub>f</sub>	V <sub>GE</sub> = ± 15 V, T <sub>J</sub> = 25 °C	-	60	-	
Turn-on switching loss	E <sub>on</sub>		-	2.1	-	
Turn-off switching loss	E <sub>off</sub>		-	2.6	-	
Turn-on delay time	t <sub>d(on)</sub>		-	270	-	ns ns
Rise time	t <sub>r</sub>		-	60	-	
Turn-off delay time	t <sub>d(off)</sub>	$V_{CC} = 600 \text{ V}, I_{C} = 50 \text{ A}, R_{g} = 18 \Omega,$ $V_{GE} = \pm 15 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$	-	500	-	
Fall time	t <sub>f</sub>		-	65	-	
Turn-on switching loss	E <sub>on</sub>		-	4.1	-	I
Turn-off switching loss	E <sub>off</sub>		-	4.7	-	mJ
Input capacitance	C <sub>ies</sub>		-	4.29	-	
Output capacitance	C <sub>oes</sub>	$V_{GE} = 0 \text{ V}, V_{CE} = 25 \text{ V}, f = 1.0 \text{ MHz}$	-	0.30	-	nF
Reverse transfer capacitance	C <sub>res</sub>		-	0.20	-	
SC data	I <sub>SC</sub>	$t_{\text{SC}} \le 10 \; \mu\text{s}, \; V_{\text{GE}} = 15 \; \text{V}, \; T_{\text{J}} = 125 \; ^{\circ}\text{C}, \ V_{\text{CC}} = 900 \; \text{V}, \; V_{\text{CEM}} \le 1200 \; \text{V}$	-	270	-	А
Internal gate resistance	R <sub>gint</sub>		-	10	-	Ω
Stray inductance	L <sub>CE</sub>		-	-	30	nΗ
Module lead resistance, terminal to chip	R <sub>CC'+EE'</sub>	T <sub>C</sub> = 25 °C	-	0.75	-	mΩ

<b>DIODE ELECTRICAL SPECIFICATIONS</b> (T <sub>C</sub> = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Diada famuard valtaga	V	I 50 A	T <sub>J</sub> = 25 °C	-	2.15	-	.,,
Diode forward voltage V <sub>F</sub>	$I_F = 50 \text{ A}$	T <sub>J</sub> = 125 °C	-	2.35	-	- V	
Diodo roverso recovery time		I <sub>F</sub> = 50 A, V <sub>R</sub> = 600 V,	T <sub>J</sub> = 25 °C	-	90	-	
Diode reverse recovery time	t <sub>rr</sub>		T <sub>J</sub> = 125 °C	-	130	-	ns
Diede peek reverse resevery current			T <sub>J</sub> = 25 °C	-	52	-	۸
Diode peak reverse recovery current	I <sub>RM</sub>		T <sub>J</sub> = 125 °C	-	60	-	A
Diada rayaraa raaayan anaray	_	31	T <sub>J</sub> = 25 °C	-	1.9	-	ml
Diode reverse recovery energy	E <sub>rec</sub>		T <sub>J</sub> = 125 °C	-	4.0	-	mJ

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature range	TJ		-40	-	150	°C
Storage temperature range	T <sub>Stg</sub>		-40	-	125	
Junction to case IGBT	р		-	-	0.28	
per ½ module Diode	R <sub>thJC</sub>		-	-	0.65	K/W
Case to sink	R <sub>thCS</sub>	Conductive grease applied	-	0.05	-	
Mounting torque		Power terminal screw: M5	2.5 to 5.0		Nm	
Mounting torque		Mounting screw: M6	;	3.0 to 6.0	)	INIII
Weight	150		150		g	



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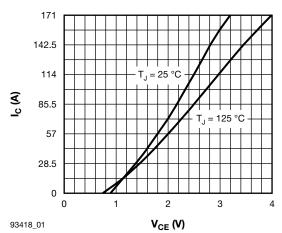


Fig. 1 - Typical Output Characteristics  $V_{GE} = 15 \text{ V}$ 

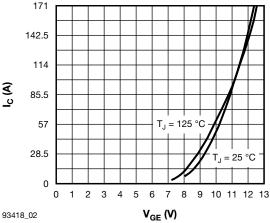


Fig. 2 - Typical Transfer Characteristics  $V_{CE} = 20 \text{ V}$ 

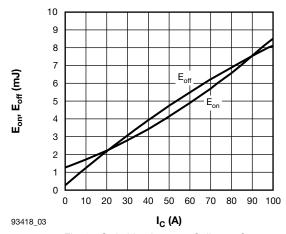


Fig. 3 - Switching Loss vs. Collector Current  $T_J=125~^{\circ}C,~V_{CC}=600~V,~V_{GE}=\pm~15~V,~R_q=18~\Omega$ 

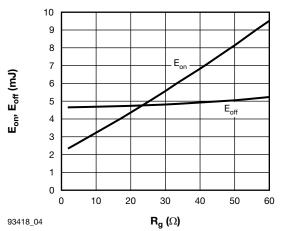


Fig. 4 - Switching Loss vs. Gate Resistance  $T_J$  = 125 °C,  $V_{CC}$  = 600 V,  $V_{GE}$  =  $\pm$  15 V,  $I_C$  = 50 A

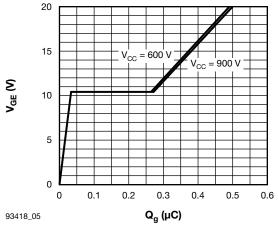


Fig. 5 - Gate Charge Characteristics  $I_C = 50 \text{ A}, T_J = 25 \,^{\circ}\text{C}$ 

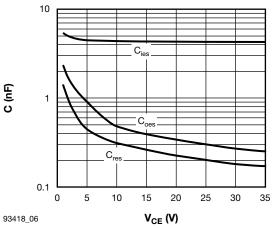


Fig. 6 - Typical Capacitance vs. Collector to Emitter Voltage



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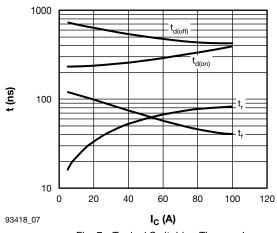


Fig. 7 - Typical Switching Time vs.  $I_C$   $T_J$  = 125 °C,  $V_{CC}$  = 600 V,  $V_{GE}$  = ± 15 V,  $R_g$  = 18  $\Omega$ 

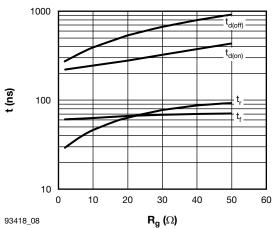


Fig. 8 - Typical Switching Time vs. Gate Resistance  $\rm T_J=125~^{\circ}C,~V_{CC}=600~V,~V_{GE}=\pm~15~V,~I_{C}=50~A$ 

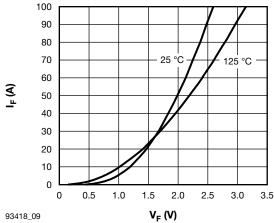


Fig. 9 - Typical Forward Characteristics (Diode)

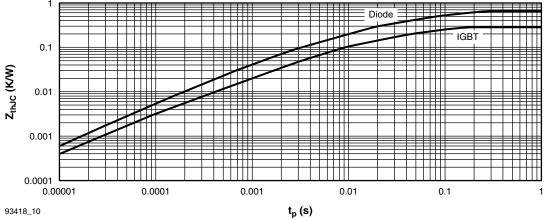
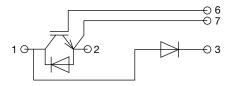


Fig. 10 - Transient Thermal Impedance

#### **CIRCUIT CONFIGURATION**

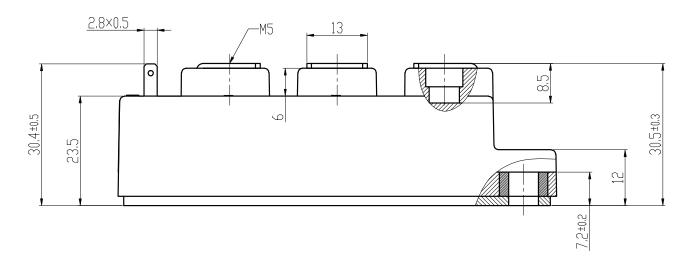


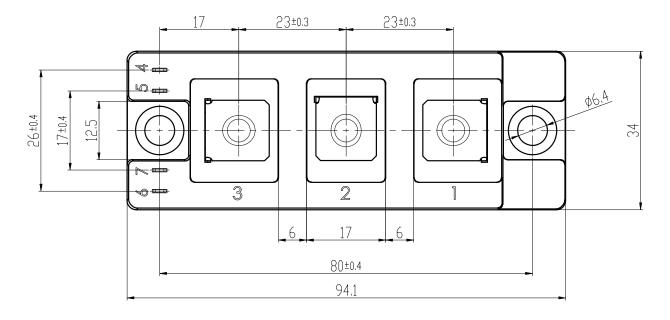
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95524			



## **INT-A-PAK**

#### **DIMENSIONS** in millimeters (inches)







## **Legal Disclaimer Notice**

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